

IN THE SPECIFICATION:

Kindly amend the specification as follows:

Please amend paragraph 0069 of the specification as follows:

-- The CPU 62 controls the operation of the digital camera 51. The CPU ~~45~~62 receives reference timing signals (the vertical synchronization signal VD1 serving as the first reference signal and the vertical synchronization signal VD2 and the horizontal synchronization signal HD1 serving as the second reference signals) from the synchronization-signal generation section 71, and generates control signals for controlling the operations of the signal processing section 13, the memory controller 61, and the display-signal processing section 63 and outputs them, and generates the address setting signal for the memory 19 and outputs it to the memory controller 61.--

Please amend paragraph 0063 of the specification as follows:

-- The digital camera 51 shown in FIG. 2 has basically the same structure as the digital camera 1 shown in FIG. 1 except that a memory controller 61 is provided instead of the memory controller ~~44~~14, a CPU 62 is provided instead of the CPU 15, a display-signal processing section 63 having a synchronization-signal generation section 71 is provided in stead of the display-signal processing section 20 having the synchronization-signal generation section 31, and a codec-IC interface 64 is newly provided.--